

**AMENDMENTS TO THE CLAIMS**

**Claim 1 (Canceled)**

**Claim 2 (Previously presented):** The flat panel display of claim 16, further comprising:

an insulating dielectric layer interposed between the inside surfaces of said front and glass plates.

**Claim 3 (Canceled)**

**Claim 4 (Previously presented):** The flat panel display of claim 16, wherein said metal heater layer is patterned onto said TFT array layer.

**Claim 5 (Previously presented):** The flat panel display of claim 16, wherein said metal heater layer is behind said black mask EMI layer.

**Claim 6 (Canceled)**

**Claim 7 (Previously presented):** The flat panel display of claim 16, wherein said metal heater layer comprises a grid of intersecting horizontal and vertical lines.

**Claim 8 (Previously presented):** The flat panel display of claim 16, further comprising an insulating dielectric over-coated onto said metal heater layer.

**Claim 9 (Previously presented):** The flat panel display of claim 16, further comprising:

at least one thermal sensor integral to said TFT array layer.

**Claim 10 (Previously presented):** The flat panel display of claim 9, wherein each said thermal sensor comprises of an array of diodes.

**Claim 11 (Canceled)**

**Claim 12 (Previously presented):** A flat panel display comprising:

- a front plate;
- a rear plate;
- a layer of liquid crystals interposed between said front and rear glass plates;
- a TFT array layer interposed between said front and rear glass plates; and
- a metal heater layer integral to said TFT array layer.

**Claim 13 (Original):** A flat panel display according to claim 12, wherein said metal heater layer is patterned onto said TFT array layer.

**Claim 14 (Original):** A flat panel display according to claim 12, where said metal heater layer is comprised of a grid of intersecting horizontal and vertical lines.

**Claim 15 (Canceled)**

**Claim 16 (Original):** A flat panel display according to claim 12, further comprising:

a black mask EMI layer interposed between said front and rear glass plates and wherein said black mask EMI layer is electrically tied to zero potential and isolated from Vcom.

**Claim 17 (Canceled)**

**Claim 18 (Previously presented):** The flat panel display of claim 12, further comprising:

at least one thermal sensor integral to said TFT array layer.

**Claim 19 (original):** The flat panel display of claim 18, wherein said at least one thermal sensor is applied onto said TFT array layer.

**Claim 20 (original):** The flat panel display of claim 18, wherein each said at least one thermal sensor comprises an array of diodes.

**Claim 21 (Canceled)**